



1512 1765

Appl. No. : 10/081,963 Confirmation No. 5115
Applicant : Jon Geist
Filed : Feb. 21, 2002
Art Unit : 1765
Examiner : Duy Vu Nguyen Deo
Title : Method for TMAH etching of CMOS integrated circuits

Commissioner of Patents
PO Box 1450
Alexandria VA 22313-1450

AMENDMENT A (corrected)

Sir:

In response to the Office Action of July 01, 2004, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.

Petition for Extension of Time under 37 CFR 1.136(a) follows page 3 of this paper.